

**FIGURE 1**  
(Prior Art)

/Duc Ha/ (06/07/2009)

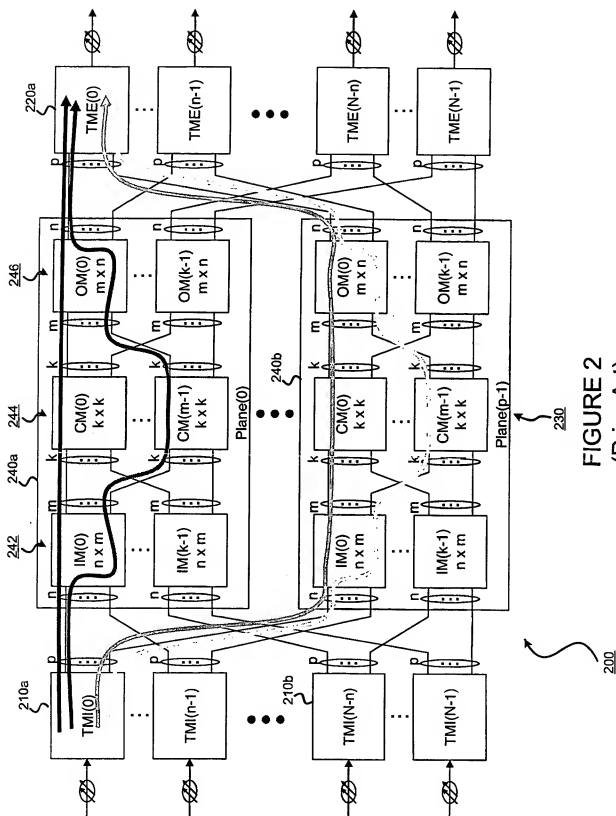


FIGURE 2  
 (Prior Art)

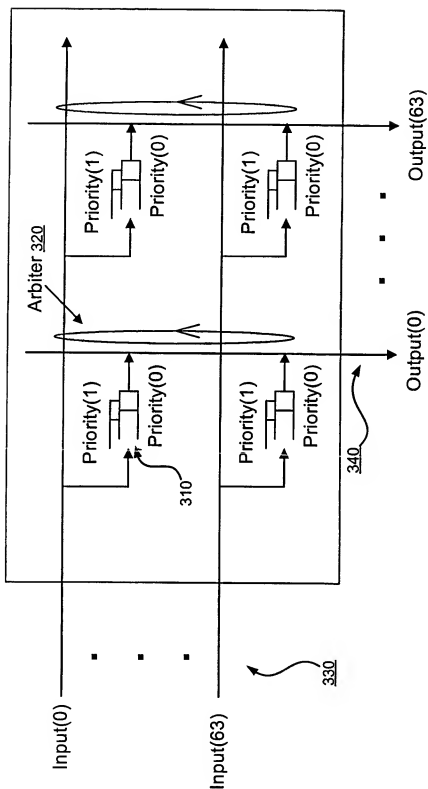
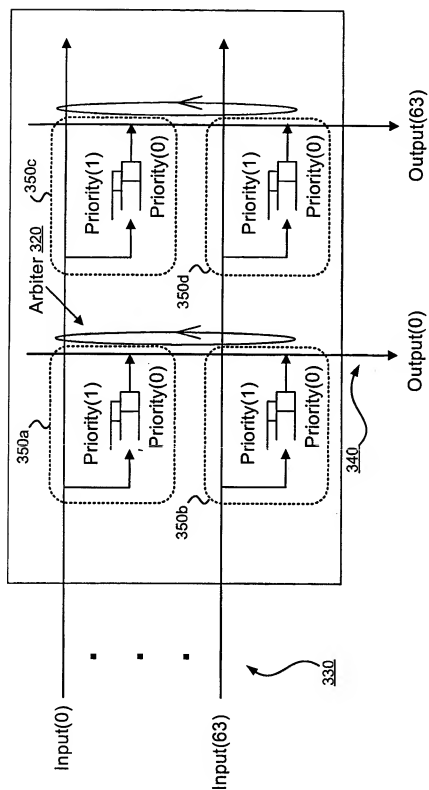


FIGURE 3A  
(Prior Art)



**FIGURE 3B**  
(Prior Art)

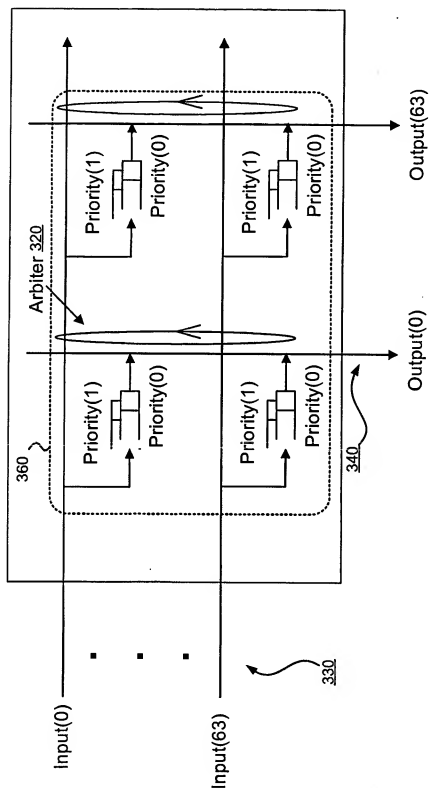


FIGURE 3C  
(Prior Art)

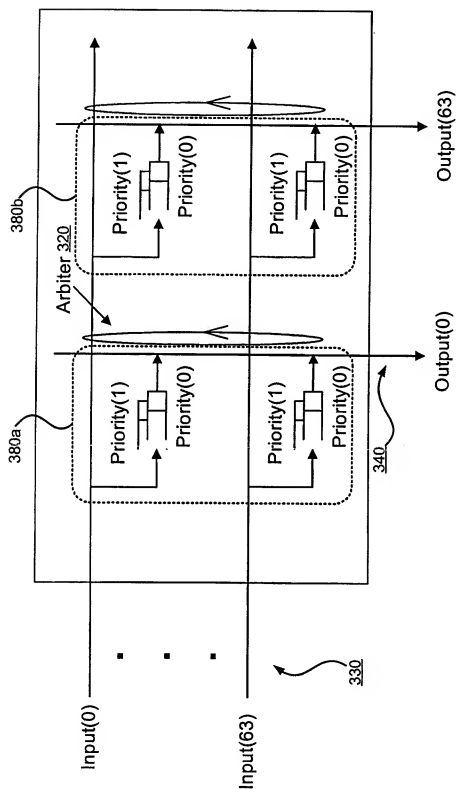


FIGURE 3D  
(Prior Art)

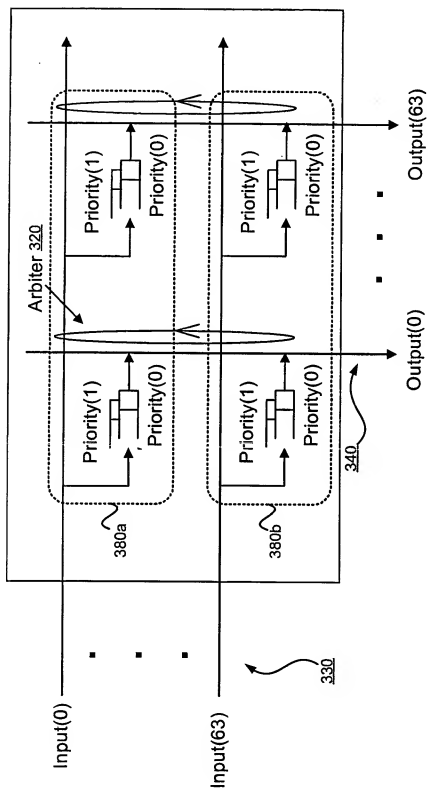


FIGURE 3E  
(Prior Art)

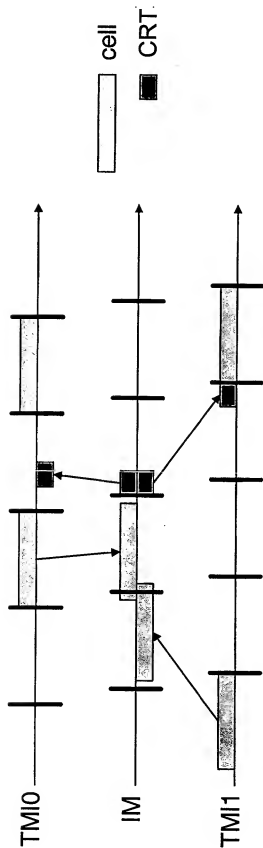


FIGURE 4  
(Prior Art)



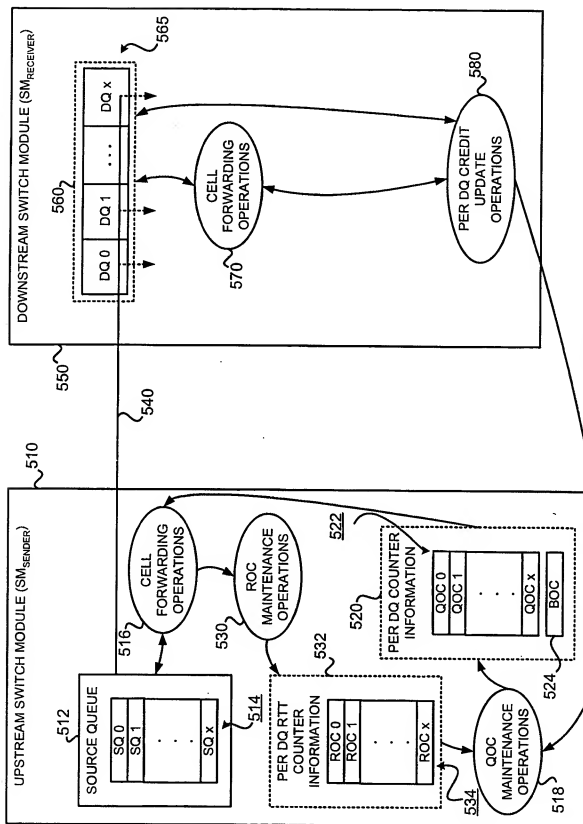


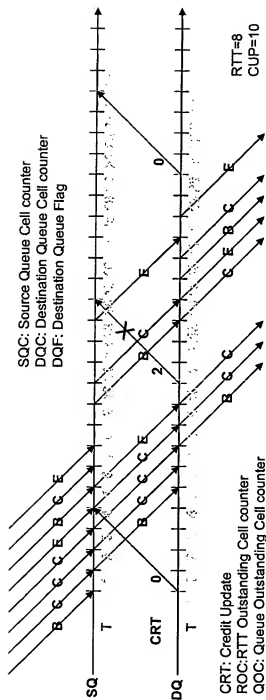
FIGURE 5

Figure 1 illustrates the operation of the Credit Based Queueing (CBQ) algorithm. The diagram shows two parallel timelines for the Source Queue (SQ) and Destination Queue (DQ). The SQ timeline includes markers for Credit Updates (C), Credit Resets (R), and Queue Outstanding Cell Counter (QOC). The DQ timeline includes markers for Credit Updates (C), Credit Resets (R), and Queue Outstanding Cell Counter (QOC). The diagram shows how credits are allocated and used to schedule packets from different sources into the destination queue. Key parameters shown are RTT=8 and CUP=10. The diagram is divided into two parts: 'CREDIT UPDATE' and 'CREDIT RESET'.

**FIGURE 6B**

[illegible]

FIGURE 7A



TS	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
SQC	0	1	1	1	1	1	1	2	3	3	2	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
DQC	0	0	0	0	0	0	1	2	2	2	2	2	2	2	2	2	1	0	1	0	0	0	0	0	0	0
DQF	0	0	1	1	1	1	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
CRT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ROC	0	0	1	2	3	4	0	0	0	0	1	2	2	2	3	3	0	0	0	0	0	0	0	0	0	0
QOC	0	0	1	2	3	4	5	5	5	6	7	7	7	8	8	8	8	8	8	8	8	8	8	8	8	0

FIGURE 7B



[illegible]

FIGURE 9B

	T8	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
SQC	0	1	1	1	1		1	1	2	3	3	2	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
DQC	0	0	0	0	0		0	1	2	2	2	1	1	1	0	1	1	0	1	0	0	0	0	0	0	0	0
DFC	0	0	1	1	1		1	0	0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
CRT	/	0	0	0	0		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
RCC	0	0	1	2	3		4	0	0	0	0	1	2	2	3	3	0	0	0	0	0	0	0	0	0	0	0
GOC	0	0	1	2	3		4	5	5	5	5	6	7	7	8	4	4	4	4	4	4	-	4	4	4	4	0

FIGURE 10

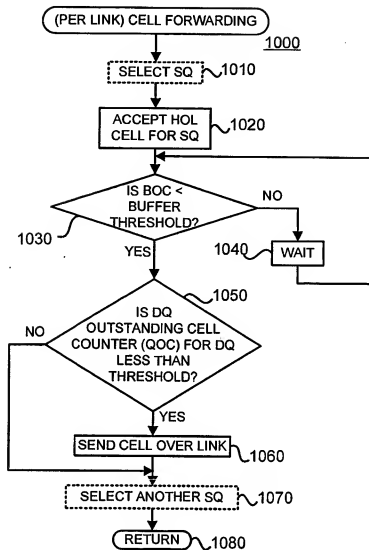
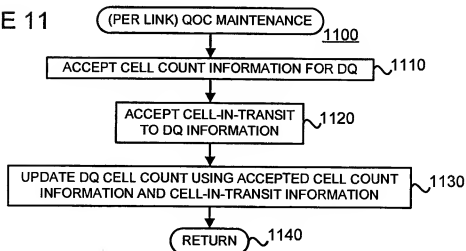


FIGURE 11



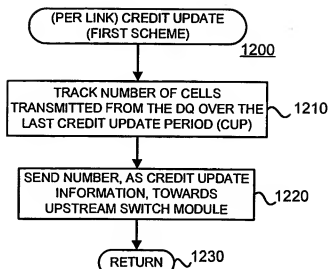


FIGURE 12A

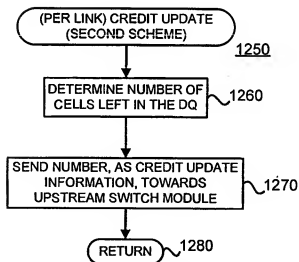


FIGURE 12B

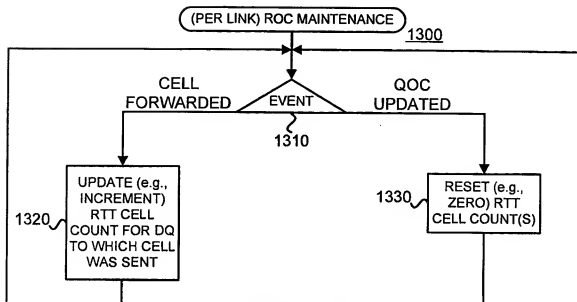


FIGURE 13